SPECIFICATION
FOR
LCD MODULE

Customer : _____

CustomerP/N _____

Model No. : B24C10638Q-01-C

Version : V00

Date : 2024-12-30

Final Approval by Customer

LCM Machinery OK ☐	Checked By	
LCM Display OK ☐	Checked By	
LCM NG ☐ LCM OK ☐	Approved By	

ShenZhen BELIJ Confirmed :

DESIGN	CHECK	APPROVAL

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1 REVISION HISTORY

Version	Date	Description	Revised By
V00	2024-12-30	First Issued	LQ

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2 GENERAL INFORMATION

Item	Specification	Unit
LCD size 液晶面板尺寸	10.1	inch
Display Mode 显示模式	Normally Black	--
Resolution 分辨率	800(RGB)x1280	Pixel
Pixel pitch 像素尺寸	0.1692(H) x 0.1692(V)	mm
Pixel Arrangement 像素排列	RGB Vertical Stripe	
Viewing direction 视角	Free	-
Module outline dimension	143 (H)*228.6 (V)*2.6(D)（LCM） 156.99 (H)*238.19 (V)*3.26(TLCM)	mm
LCD AA 液晶显示区域	135.36 (H)* 216.58 (V)	mm
TP VA 触摸可视区域	136.16(H)*217.38(V)	mm
Colors 颜色深度	16.7M	-
Weight 重量(TP+LCM)	TBD	g
Driver IC 驱动芯片	HX83102G	TDDI
Driver IC RAM Sise 记忆体	不包含	-
Interface 接口类型	MIPI	--
Backlight 背光类型	White LED	--
Touch IC 触摸芯片	HX83102G	TDDI
Surface hardness 表面硬度	6H	--
支持点数	10点	--
Touch structure 触摸结构	INCELL	--
Cover lens 盖板	0.7T	--
Colors 颜色	BLACK	--
Operating Temperature 工作温度	-10℃~ +50℃	--
Storage Temperature 存储温度	-20℃~ +60℃	--
环保要求	ROHS	--

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3 ABSOLUTE MAXIMUM RATINGS

The absolute maximum ratings are listed on Table 8.1. When used out of the absolute maximum ratings, the LSI may be permanently damaged. Using the LSI within the following electrical characteristics limit is strongly recommended for normal operation. If these electrical characteristic conditions are exceeded during normal operation, the LSI will malfunction and cause poor reliability.

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Power supply voltage 1(1),(2)	VDD1~VSSD	-0.3	-	+2.15	V
Power supply voltage 2(3)	VSP~VSSA	-0.3	-	+6.5	V
Power supply voltage 3(4)	VSSA~VSN	0	-	-6.5	V
Power supply voltage 4(5)	VGH~VSSA	-0.3	-	+20	V
Power supply voltage 5(6)	VSSA~VGL	-18	-	0	V
Operating temperature(7)	T_A	-20	-	+85	°C
Storage temperature(8)	Tstg	-55	-	+110	°C
Input voltage(9)	V_{IN}	-0.3	-	VDD1+0.3	V
HS input voltage(10)	V_{HSIN}	-0.3	-	+2	V

Note: (1) VDD1, VSSD must be maintained.
(2) To make sure VDD1 ≥ VSSD.
(3) To make sure VSP ≥ VSSA.
(4) To make sure VSSA ≥ VSN
(5) To make sure VGH ≥ VSSA.
(6) To make sure VSSA ≥ VGL, VGH +|VGL| < 32V.
(7) For die and wafer products, specified up to +85°C.
(8) This temperature specifications apply to the COG package.
(9) This specifications include input signals but without following: CP, CN, D0P, D0N, D1P, D1N, D2P, D2N, D3P, D3N.
(10) This specifications include following signals: CP, CN, D0P, D0N, D1P, D1N, D2P, D2N, D3P, D3N.

Table 8.1: Absolute maximum rating

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Parameter	Symbol	Min	Max	Unit	Note
Power Supply voltage 1	VSP~GND	-0.3	+6.5	V	
Power Supply voltage 2	VSN~GND	-0.3	+6.5	V	
Power Supply voltage 3	IOVCC~GND	-0.3	+2.15	V	
Logic Input Voltage Range	V_{IN}	-0.3	IOVCC+0.3	V	
Logic Output Voltage Range	V_O	-0.3	IOVCC+0.3	V	
Operating temperature	Topr	-10	+50	℃	
Storage temperature	Tstg	-20	+60	℃	

* The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme

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case, the module may be permanently destroyed.

4 DC ELECTRICAL CHARACTERISTICS

4.1 Driving TFT LCD Panel

8.2. DC characteristics

Parameter	Symbol	Condition	Spec. Min.	Spec. Typ.	Spec. Max.	Unit
Input high voltage	V_{IH}	VDD1=1.65~1.95V	0.7 VDD1	-	VDD1	V
Input low voltage	V_{IL}		0	-	0.3 VDD1	V
Output high voltage	V_{OH}	I_{OH}=-1.0 mA	0.8 VDD1	-	VDD1	V
Output low voltage	V_{OL}	VDD1=1.65~1.95V I_{OL}=1.0 mA	0	-	0.2 VDD1	V
Source offset voltage	V_{OFFSET}	VDD1=1.8V VSP=5.5V VSN=-5.5V T_A=25°C	-	-	±4	mV
Logic High level input current	I_{IH}	All logic input pins	-	-	1	μA
Logic Low level input current	I_{IL}	All logic input pins	-1	-	-	μA
VDD1 current consumption in sleep in mode	$I_{STB(VDD1)}$	VDD1=1.8V VSP=5.5V VSN=-5.5V T_A=25°C Note(1)	-	-	450	μA
VSP current consumption in sleep in mode	$I_{STB(VSP)}$		-	-	150	μA
VSN current consumption in sleep in mode	$I_{STB(VSN)}$		-	-	55	μA
VDD1 current consumption in ULPS mode	$I_{ULPS(VDD1)}$	VDD1=1.8V VSP=5.5V VSN=-5.5V T_A=25°C Note(1)	-	-	425	μA
VSP current consumption in ULPS mode	$I_{ULPS(VSP)}$		-	-	50	μA
VSN current consumption in ULPS mode	$I_{ULPS(VSN)}$		-	-	50	μA
Oscillator	OSC	T_A=25°C	-3	-	3	%
Oscillator variation relative to Room Temperature tolerance **(OSC tracking disable)**	ΔOSC	T_A=-20~+85°C	-	-	±5	%
Oscillator variation relative to Room Temperature tolerance **(OSC tracking enable)**	ΔOSC	T_A=-20~+85°C	-	-	±0.5	%

Note: (1) TP_RESX pull low and MIPI lanes keep LP11.

Table 8.2: DC characteristics

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AGND = GND = 0V, Ta = 25℃

4.2 Backlight Characteristics

Ta = 25℃

Item	Symbol	Min	Typ	Max	Unit	Condition
Forward voltage	Vf	22.6	24	25.6	V	If=90 mA
Number of LED	-	8X4			Piece	-
Connection mode	S/P	8Serial/4Parallel			-	-

Using condition: constant current driving method If= 4×20mA (+/-10%)

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5 TIMING CHARACTERISTICS

5.1 High Speed Mode-Clock Channel Timing

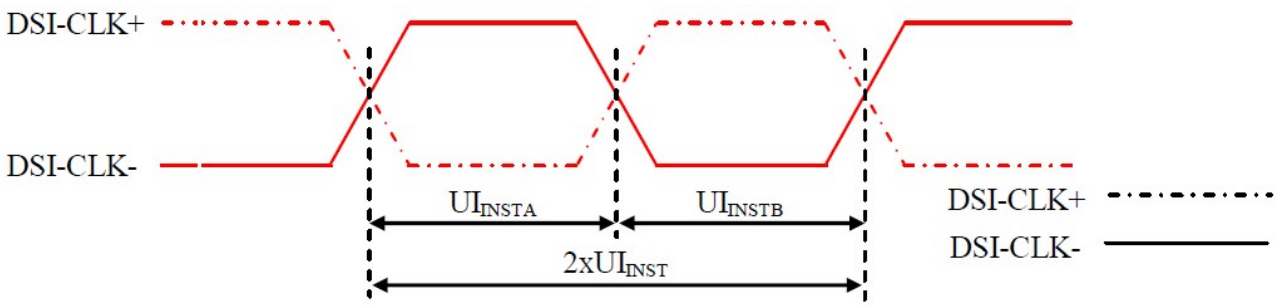

Figure 5.1.1 DSI Clock Channel Timing

Signal	Symbol	Parameter	Min	Max	Unit
CLKP/N	$2xUI_{INST}$	Double UI instantaneous	4	25	ns
CLKP/N	UI_{INSTA},UI_{INSTB} (Note 1)	UI instantaneous Half	2 (Note 2)	12.5	ns

Notes:

1. UI = UIINSTA = UIINSTB

2. Define the minimum value of 24 UI per Pixel, see Table 39.

Table 39: Limited Clock Channel Speed

Data type	Two Lanes speed	Three Lanes speed	Four Lanes speed
Data Type = 00 1110 (0Eh), RGB 565, 16 UI per Pixel	566 Mbps	433 Mbps	366 Mbps
Data Type = 01 1110 (1Eh), RGB 666, 18 UI per Pixel	637 Mbps	487 Mbps	412 Mbps
Data Type = 10 1110 (2Eh), RGB 666 Loosely, 24 UI per Pixel	850 Mbps	650 Mbps	550 Mbps
Data Type = 11 1110 (3Eh), RGB 888, 24 UI per Pixel	850 Mbps	650 Mbps	550 Mbps

5.2 High Speed Mode-Data Clock Channel Timing

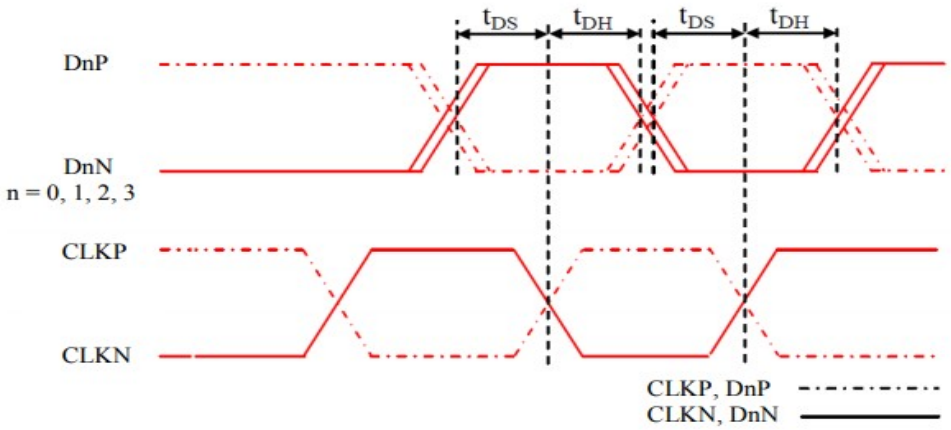

Figure 5.2.1 Data to Clock Channel Timing

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Signal	Symbol	Parameter	Min	Max
DnP/N , n=0 and 1	t_{DS}	Data to Clock Setup time	0.15xUI	-
	t_{DH}	Clock to Data Hold Time	0.15xUI	-

5.3 High Speed Mode-Rising and Falling Timings

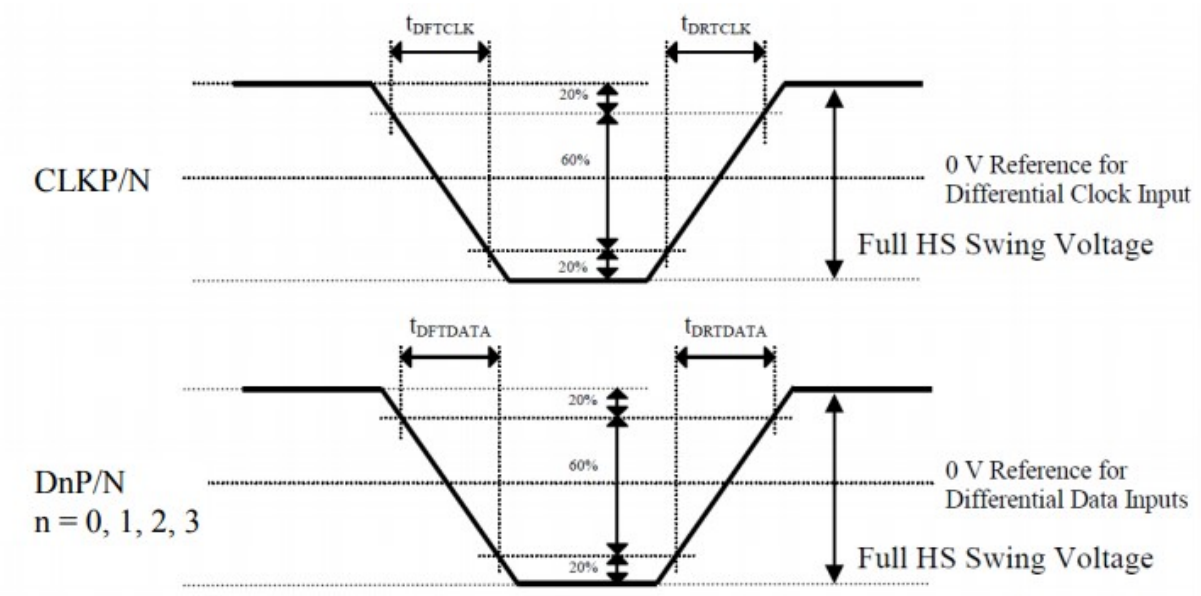

Figure 5.3.1 Rising and Falling Timings on Clock and Data channels

Parameter	Symbol	Condition	Specification		
			Min	Typ	Max
Differential Rise Time for Clock	t_{DRTCLK}	CLKP/N	150 ps	-	0.3UI (Note)
Differential Rise Time for Data	$t_{DRTDATA}$	DnP/N n=0 and 1	150 ps	-	0.3UI (Note)
Differential Fall Time for Clock	t_{DFTCLK}	CLKP/N	150 ps	-	0.3UI (Note)
Differential Fall Time for Data	$t_{DFTDATA}$	DnP/N n=0 and 1	150 ps	-	0.3UI (Note)

Note: *The display module has to meet timing requirements, which are defined for the transmitter (MCU) on MIPI D-Phy standard.*

5.4 VSP VSN VDDI(IOVCC) Input power sequence

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5.10.2. Power on sequence for display initial code by Host

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
VDD1 / HS_VCC to VSP	T1	1	-	-	ms
VSP to VSN	T2	1	-	-	ms
VDD1 / HS_VCC to MIPI Lane	T3	1	-	-	ms
Power Ready to Global Reset	T4	1	-	-	ms
RESX Keep Low	T5	15(1)	-	-	μs
RESX to Display Initial Code By HOST	T6	5	-	-	ms
Sleep Out to Display On	T7	10	-	-	ms
Display On to IC Ready	T8	20	-	-	ms
Display On Command to BL On time	tBLON	40	-	-	ms

Note: (1) TP Reset is the same.

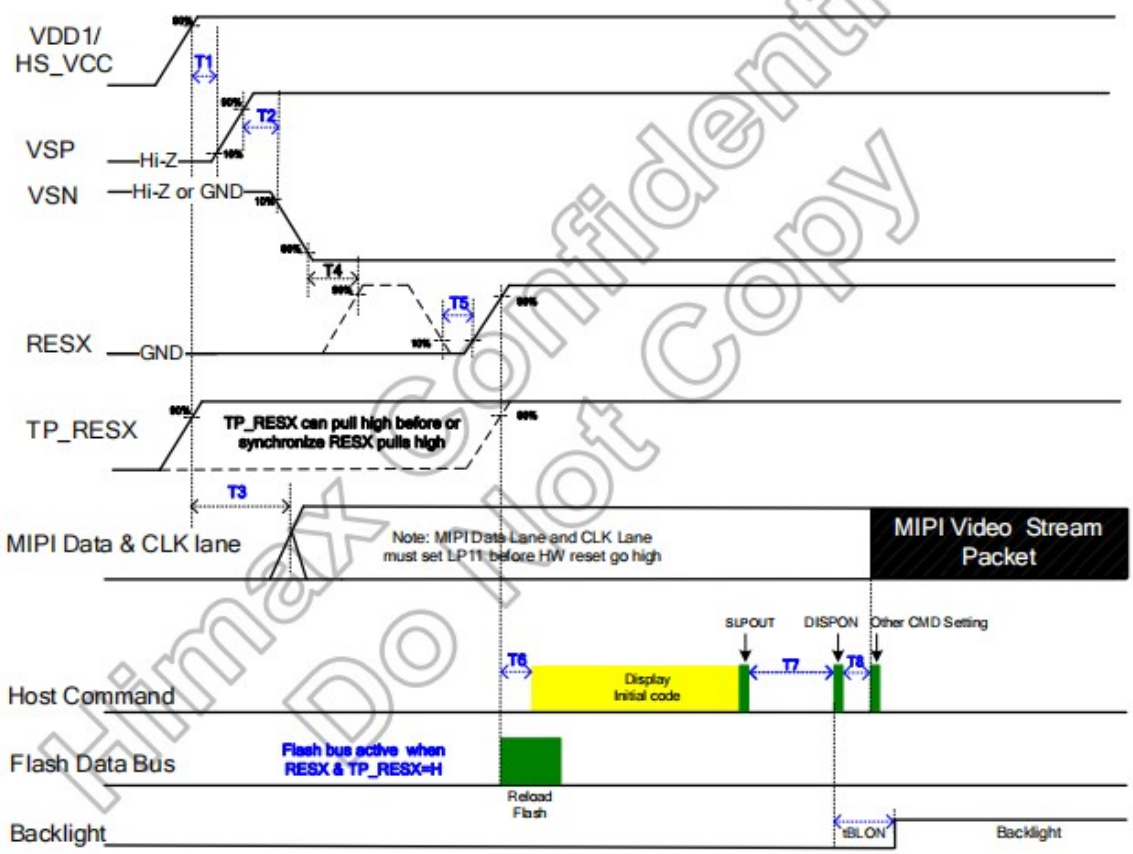

Figure 5.15: Power on sequence for display initial code by Host

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5.10.4. Internal power on sequence

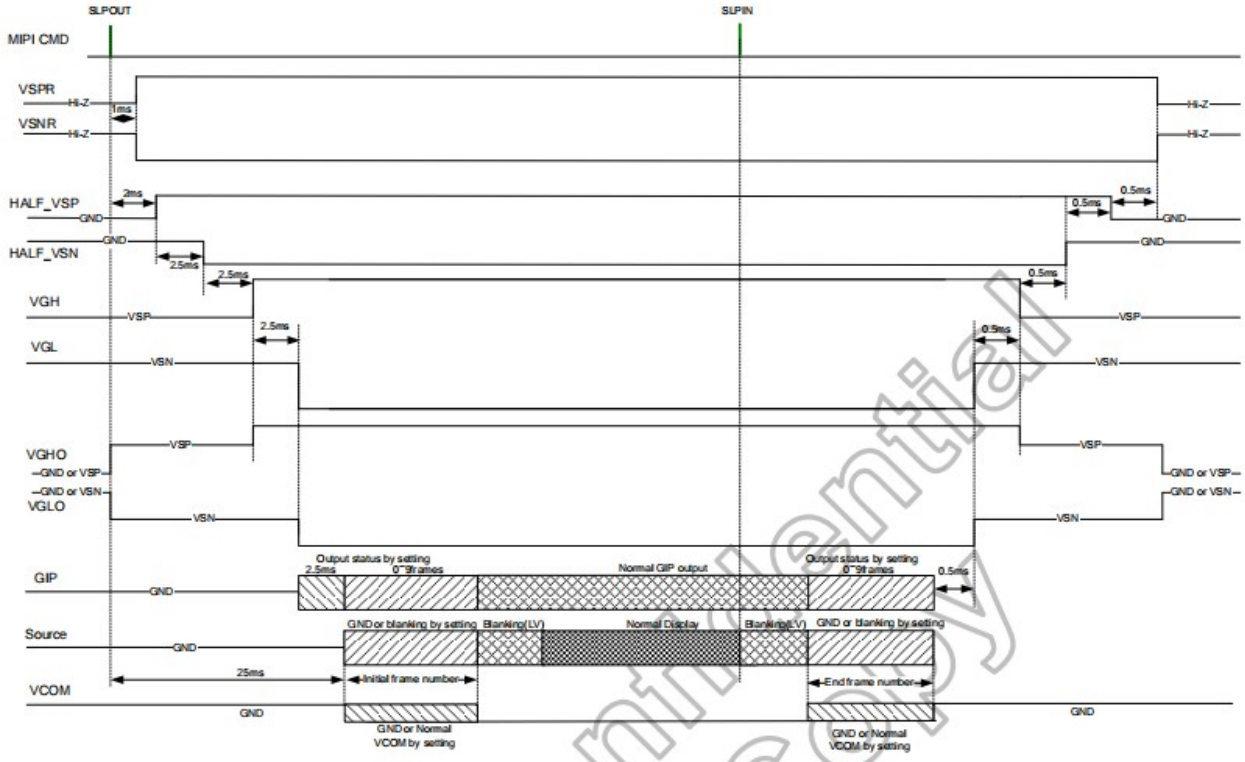

Figure 5.17: Internal power on sequence

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5.10.6. Power off sequence

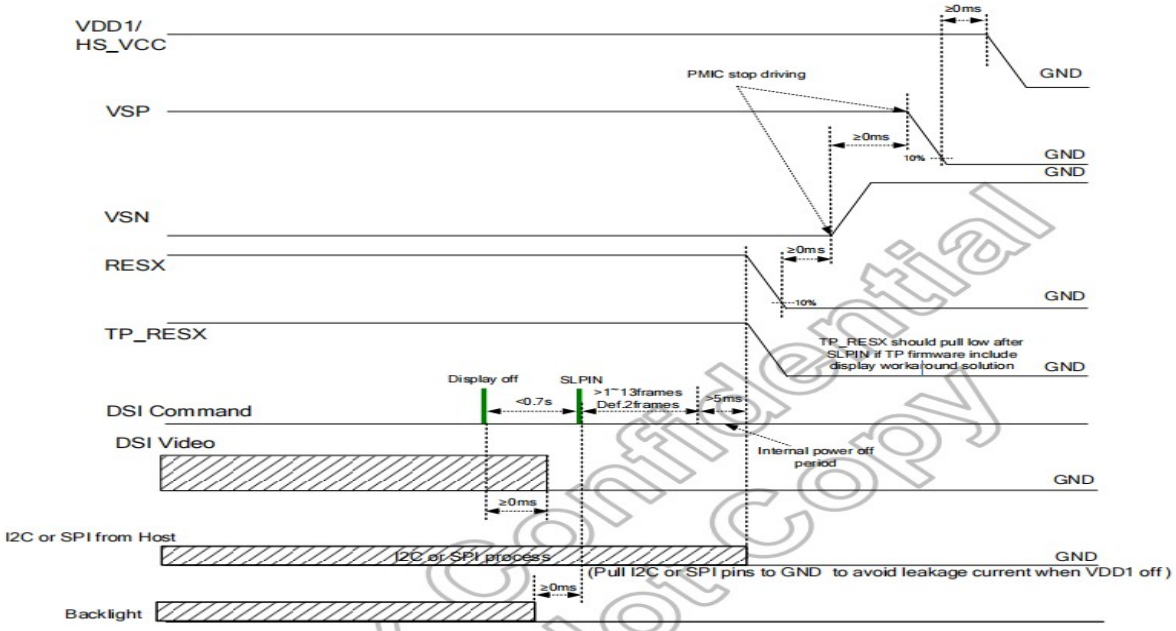

Figure 5.19: Power off sequence

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5.10.7. Power off to power on sequence

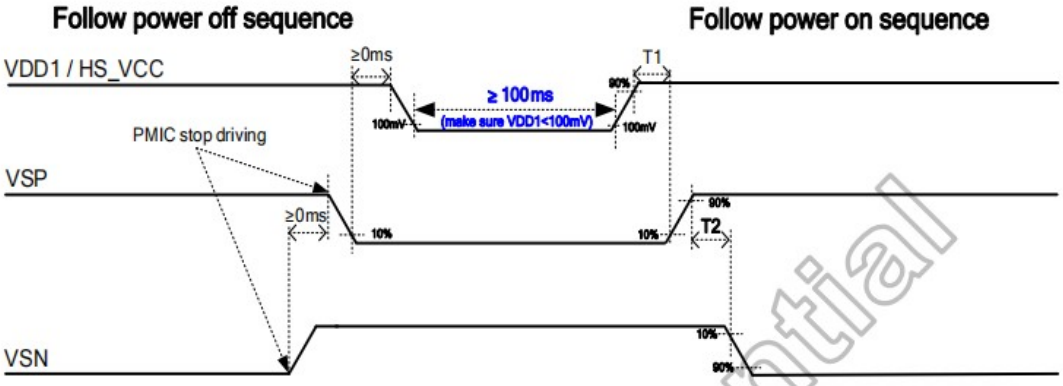

Figure 5.20: Power off to power on sequence

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
VDD1 / HS_VCC to VSP	T1	1	-	-	ms
VSP to VSN	T2	1	-	-	ms

Table 5.8: Power off to power on timing

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8.3. MIPI DC characteristics

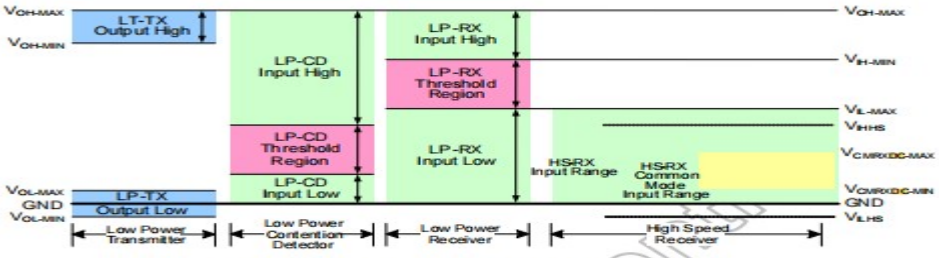

Figure 8.1: MIPI signaling and contention voltage levels

DC characteristics for MIPI LP mode

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Logic 1 input voltage	V_{IH}	880	-	-	mV
Logic 0 input voltage	V_{IL}	0	-	550	mV
Logic 1 output voltage	V_{OH}	1.1	1.2	1.3	V
Logic 0 output voltage	V_{OL}	-50	-	50	mV

Table 8.3: DC characteristics for MIPI LP mode

DC characteristics for MIPI HS mode

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Common-mode voltage HS Receive mode	V_{CMRXDC}	70	-	330	mV
Differential input high threshold(1)	V_{IDTH}	70	-	-	mV
Differential input low threshold(1)	V_{IDTL}	-	-	-70	mV
Single-ended input high voltage	V_{IHHS}	-	-	460	mV
Single-ended input low voltage	V_{ILHS}	-40	-	-	mV
Differential input impedance	Z_{ID}	80	100	125	Ω
TX HS transmit differential voltage (VDP-VDN)	|VOD|	140	200	270	mV

Note: (1) V_{IDTH} and V_{IDTL} are only for reference, related to power and ground noise, this spec need to check on panel performance to fine tune

Table 8.4: DC characteristics for MIPI HS mode

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8.4. AC characteristics

8.4.1. MIPI AC characteristics

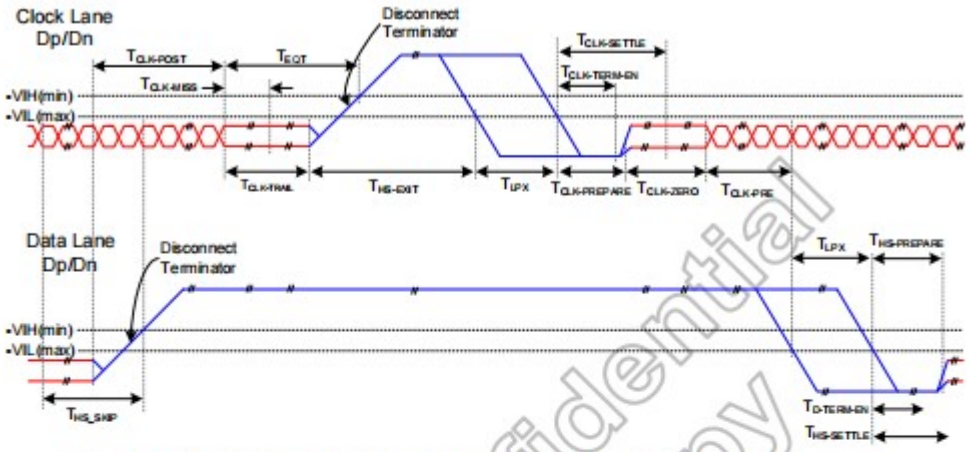

Figure 8.2: Switching the clock lane between clock transmission and low-power mode

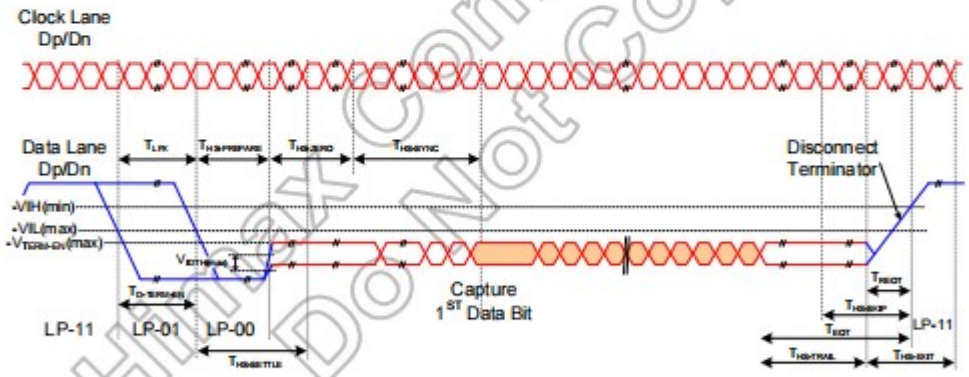

Figure 8.3: Timing of high-speed data transmission in bursts

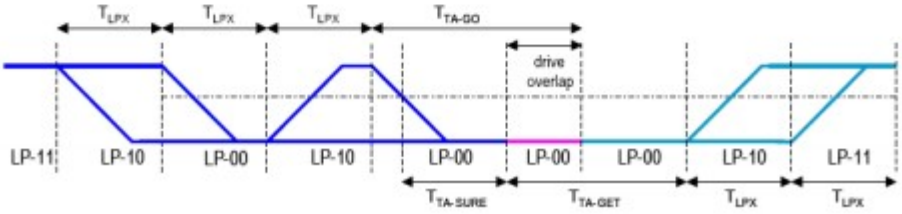

Figure 8.4: Turnaround procedure

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DATA SHEET V01

8.4.2. MIPI data-clock timing specification

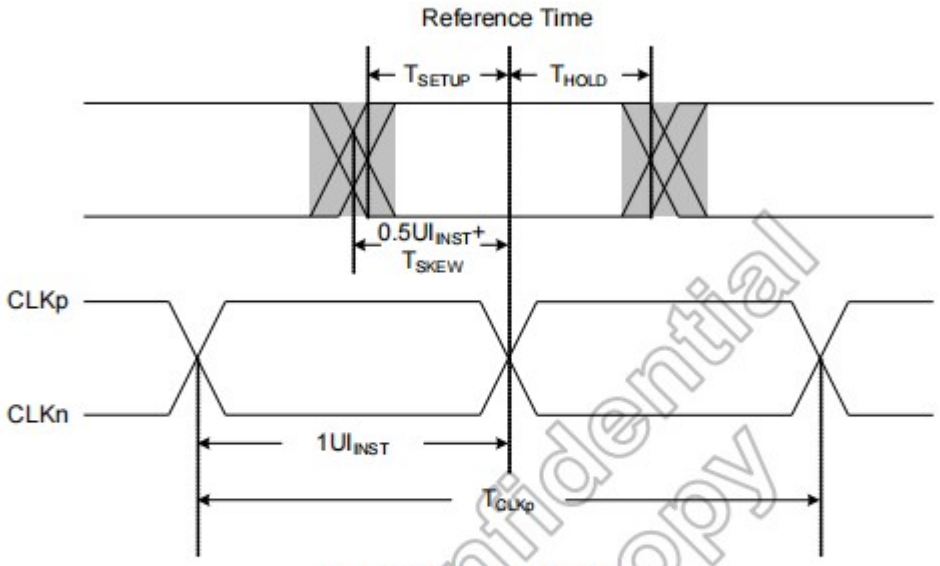

Figure 8.5: Data to clock timing

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
UI instantaneous	UI_{INST}	0.909	-	12.5(1)	ns
Data to clock setup time	T_{SETUP}	0.15(2)	-	-	UI_{INST}
Data to clock hold time	T_{HOLD}	0.15(2)	-	-	UI_{INST}

Note: (1) This value corresponds to a minimum 80 Mbps data rate.
(2) Total SETUP and HOLD window for receiver of 0.3* UI_{INST}

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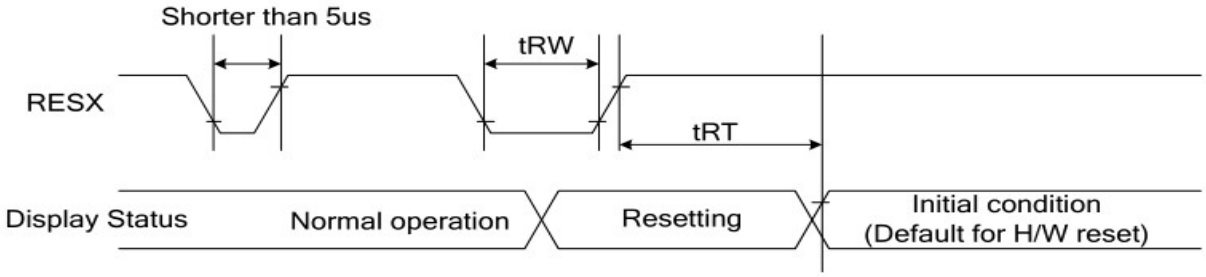
6. Reset Timing



Signal	Symbol	Parameter	Min	Max	Unit
RESX	tRW	Reset pulse duration	10		uS
	tRT	Reset cancel		5 (note 1,5)	mS
				120 (note 1,6,7)	mS

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Notes:

1. The reset cancel also includes required time for loading ID bytes, VCOM setting and other settings from EEPROM (or similar device) to registers.

This loading is done every time when there is HW reset cancel time (tRT) within 10 ms after a rising edge of RESX.

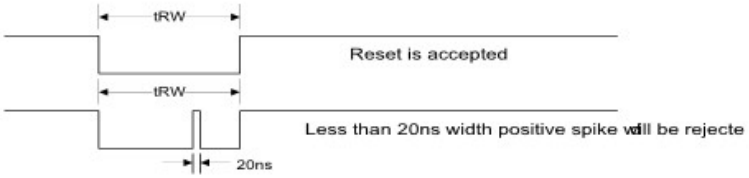
2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below ：

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. During the Resetting period, the display will be blanked(The display is entering blanking sequence, which maximum time is 120 ms, when Reset

Starts at Sleep-Out status. The display remains the blank state in Sleep-In mode). Then return to Default condition for Hardware Reset

4. Spike Rejection also applies during a valid reset pulse as shown below ：



5. When Reset applied during Sleep-In Mode.
6. When Reset applied during Sleep-Out Mode
7. It is necessary to wait 10ms after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120 ms.

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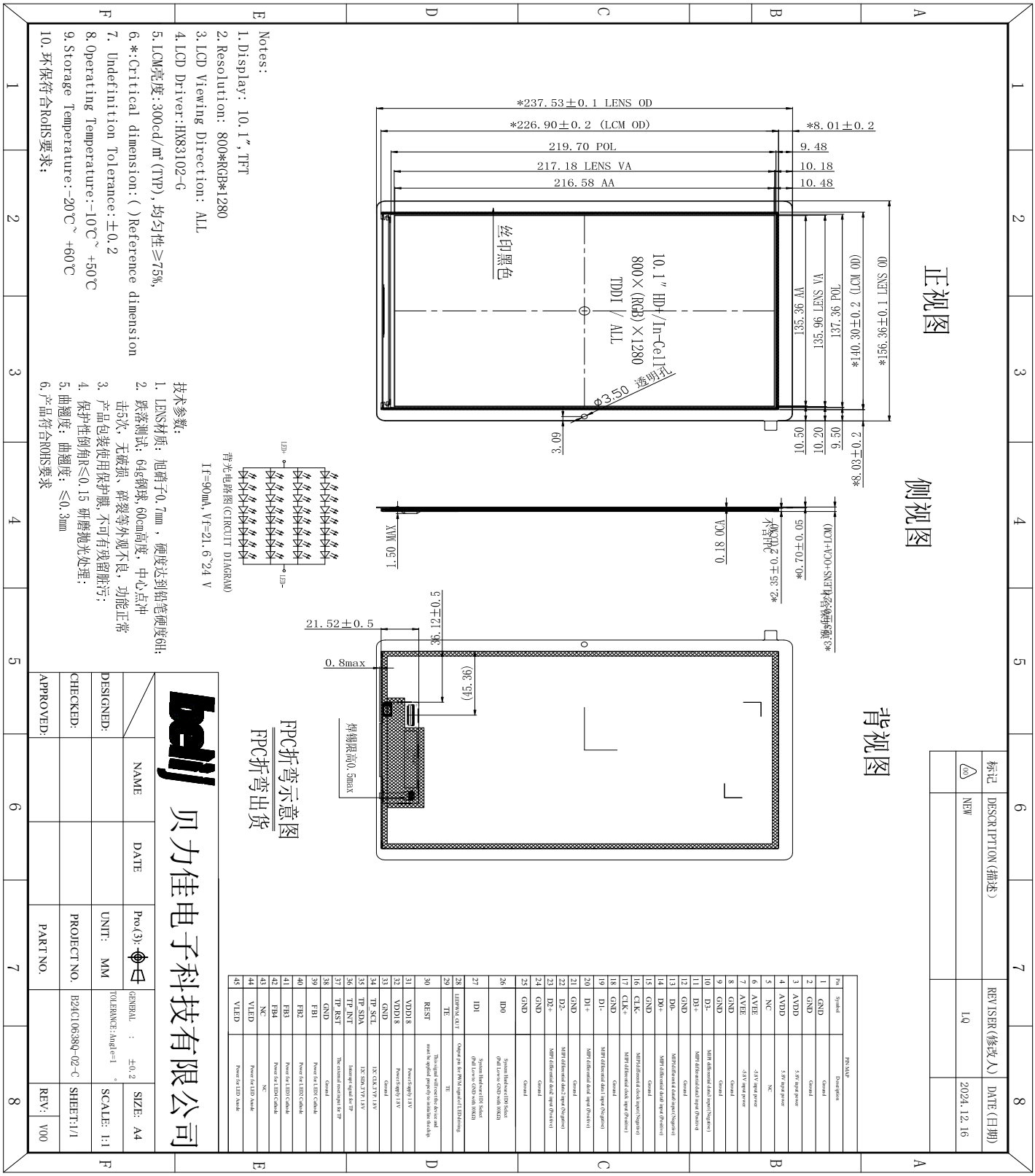
7 EXTERNALDIMENSION



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7 INTERFACE SIGNALS

Pin No.	Symbol	Description
1	GND	Power ground
2	GND	Power ground
3	AVDD	External power supply. Connect it with a stabilizing capacitor to VSSA. VSP=4.5V to 6.3V. 5.7VTYP
4	AVDD	External power supply. Connect it with a stabilizing capacitor to VSSA. VSP=4.5V to 6.3V. 5.7V TYP
5	NC	NC
6	AVEE	External power supply. Connect it with a stabilizing capacitor to VSSA. VSN= -4.5 to -6.3V,-5.7 V TYP
7	AVEE	External power supply. Connect it with a stabilizing capacitor to VSSA. VSN= -4.5 to -6.3V -5.7 V TYP
8	GND	Power ground
9	GND	Power ground
10	D3N	MIPI-DSI data lane 3 negtive input pin.
11	D3P	MIPI-DSI data lane 3 posivtve input pin.
12	GND	Power ground
13	DN0	MIPI-DSI data lane 0 negtive input pin.
14	DP0	MIPI-DSI data lane 0 posivtve input pin.
15	GND	Power ground
16	CLKN	MIPI-DSI clock negtive input pin.
17	CLKP	MIPI-DSI clock posivtve input pin.
18	GND	Power ground
19	DN1	MIPI-DSI data lane 1 negtive input pin.
20	DP1	MIPI-DSI data lane 1 posivtve input pin.
21	GND	Power ground
22	D2N	MIPI-DSI data lane 2 negtive input pin.
23	D2P	MIPI-DSI data lane 2 posivtve input pin.
24	GND	Power ground
25	GND	Power ground
26	ID0	System Hardware ID0 Select (Pull Low to GND with 10KΩ)
26	ID1	System Hardware ID0 Select (Pull Low to GND with 10KΩ)
28	**LEDPWM_OUT**	Output pin for PWM signal of LED driving.
29	TE	TE
30	RESET	Reset signal(low active)

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31	VDD1.8	Power Supply IO
32	VDD1.8	Power Supply IO
33	GND	Power ground
34	TP_SCL	I2C CLK,TYP. 1.8V
35	TP_SDAL	I2C SDA,TYP. 1.8V
36	TP_INTL	Interrupt signal for TP
37	TP_RST	The external reset input for TP
38	GND	Power ground
39	FB1	Power for LED1 Cathode
40	FB2	Power for LED1 Cathode
41	FB3	Power for LED1 Cathode
42	FB4	Power for LED1 Cathode
43	NC	NC
44	VLED	Power for LED Anode
45	VLED	Power for LED Anode

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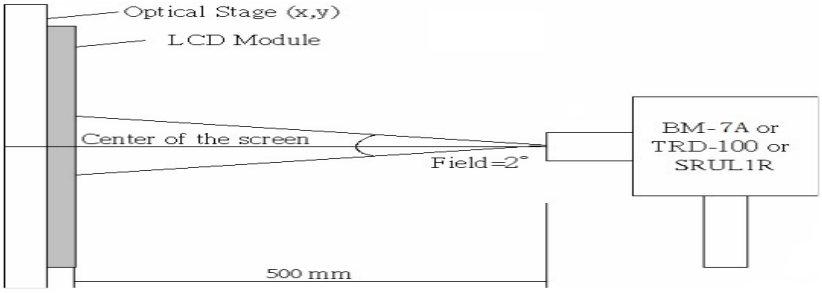
9 ELECTRO-OPTICAL CHARACTERISTICS

Item		Symbol		Condition	Min	Typ	Max	Unit	Note
Transmittance (w/o polarizer)		T%		θ=0° Ta=25℃	5.35	6.04	-	%	1
Contrast ratio		Cr			1000	1500	-	-	3
Response time		Ton+Toff			-	35	40	ms	4
Surface Luminance		LV			--	260	-	cd/㎡	2
Viewing angle range		H or V er	Θ_{x+}	Center CR>10	-	88	-	deg	5
			Θ_{x-}		-	88	-	deg	
			Θ_{Y+}		-	88	-	deg	
			Θ_{Y-}		-	88	-	deg	
CIE(x,y) chromaticity	Red	x		Viewing normal angle Θx=θy=0° Ta=25℃	-0.03	0.645	+0.03	-	6
		y				0.333		-	
	Green	x				0.324		-	
		y				0.570		-	
	Blue	x				0.134		-	
		y				0.124		-	
	White	x						-	
		y						-	

Note 1.Ambient condition：25℃±2℃，60±10%RH，under 10 Lunx in the darkroom。

Note 2.Measure device：BM-7A (TOPCON)，viewing cone=2°

Lv = Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5)



Note 3. Definition of Contrast Ratio：

$$CR = \frac{\text{Average Surface Luminance with all black pixels (P1,P2,P3,P4,P5)}}{}$$

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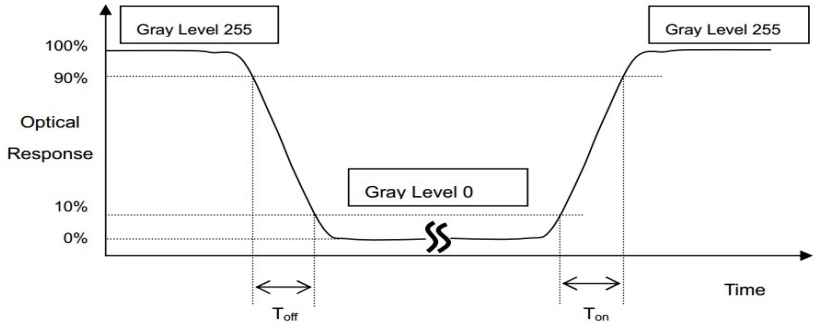
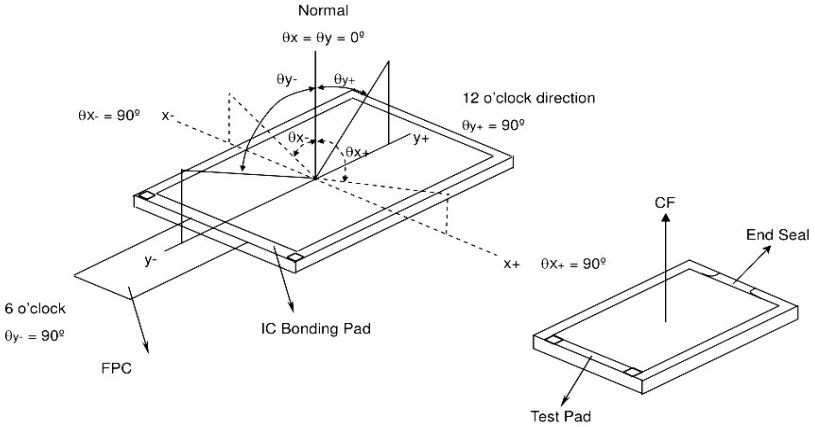
Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5)

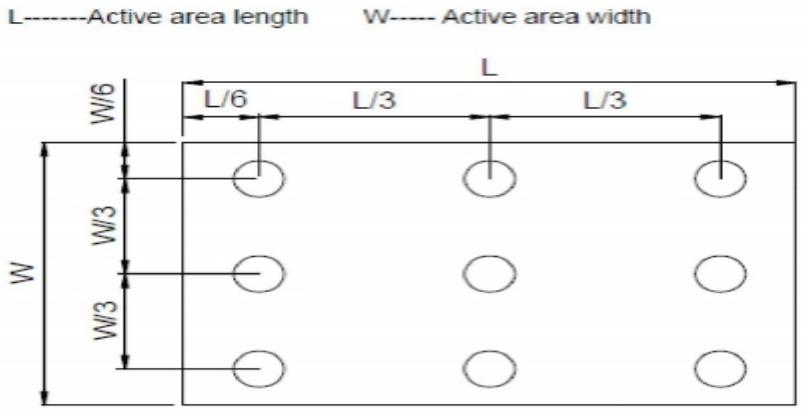
Note 4. Definition of Response Time (Ton, Toff), The response time is defined as the time interval between the 10% and 90% amplitudes.



Note 5. Definition of view angle(θ，ψ)：



Note 6. The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room. Measouring method for Contrast ratio, surface luminance, Luminance uniformity,CIE(x,y) chromaticity.



Light spot size ∅=7mm, 500mm distance from the LCD surface to detector lens measurement instrument is TOPCON's luminance meter BM-7

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10 RELIABILITY TEST

Reliability test conditions (Polarizer characteristics null)

No.	Items	Condition	Inspection after test
1	High Temperature Storage	T = 60℃for 48 hr	Inspection after 4 hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD 2.Sealleak; 3.Non-display; 4.missing segments; 5.Glass crack; 6.Current Idd is twice higher than initial value.
2	Low Temperature Storage	T = -20℃for 48 hr	
3	High Temperature Operating	T = 50℃for 48 hr	
4	Low Temperature Operating	T = -10℃for 48 hr (But no condensation of dew)	
5	High Temp. and High Humidity Storage	T = 50℃/90% for 48 hr (But no condensation dew)	
6	Thermal Shock	-20±2°C~25~60±2°C×10cycles (30min.) (5min.) (30min.)	
7	Dropping test (non-operation)	Drop to the ground from 76cm height, one time, every side of carton. (Packing condition)	
8	Packing Vibration (non-operation)	Frequency：10Hz~55Hz~10Hz Amplitude：1.0mm, X，Y，Z direction for total 3hours (Packing condition)	
9	ESD	Voltage:±6KV R: 330Ω C: 150pF Air discharge, 10time	

Note:

(1)The test samples should be applied to only one test item.

(2)Sample size for each test item is 5~10pcs.

(3)In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
Using ionizer(an antistatic blower) is recommended at working area in order to reduce electro-static voltage.
When removing protection film from LCM panel, peel off the tag slowly (recommended more than one second) while blowing with ionizer toward the peeling face to minimize ESD which may damage electrical circuit.

(4) Please use automatic switch testing mode when test operating mode.

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11 INSPECTION CRITERION

This specification is made to be used as the standard acceptance/rejection criteria for Normal LCM Product.

1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1：1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

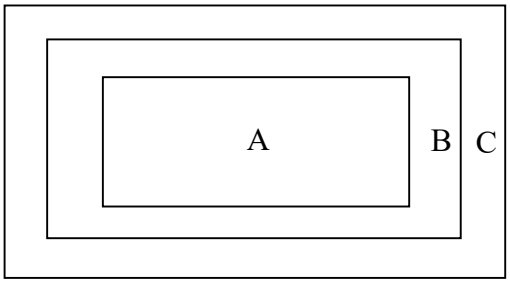
Major defect: AQL 0.65

Minor defect: AQL 1.0

2. Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature 20~25°C and normal humidity 60±15%RH).

3. Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

4.Standards of inspection items

4.1 Major Defect

Item No	Items to be inspected	Inspection Standard	Classification of defects
4.1.1	All functional defects	1.No display	Major
		2.Display abnormally	
		3.Missing vertical，horizontal segment	
		4.Short circuit	
		5. Back-light no lighting, flickering and abnormal lighting.	

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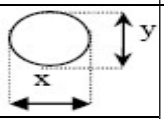
4.1.2	Missing	Missing component	
4.1.3	Outline dimension	Overall outline dimension beyond the drawing is not allowed.	
4.1.4	linearity	No more than 1.5%	

4.2 Cosmetic Defect

Item No	Items to be inspected	Inspection Standard				Classification of defects
4.2.1	Clear Spots Black and white Spot defect Pinhole, Foreign Particle, polarizer Dirt	For dark/white spot, sizeΦis defined as Φ=(x + y)/2 				Minor
		1				
		Zone / Size(mm)	Acceptable Qty			
			A	B	C	
		Φ≤0.15	Ignore		Ignore	
		0.15＜Φ≤0.20	2			
		0.20＜Φ≤0.30	1			
		Φ＞0.30	0			
	Clear Spots TP Dirt	2				Minor
		Zone / Size(mm)	Acceptable Qty			
			A	B	C	
		Φ≤0.15	Ignore		Ignore	
		0.15＜Φ≤0.20	2			
		0.20＜Φ≤0.30	1			
		Φ＞0.30	0			
	Dim Spots Circle shaped and dim edged defects	3				Minor
		Zone / Size(mm)	Acceptable Qty			
			A	B	C	
		Φ≤0.2	Ignore		Ignore	
		0.20＜Φ≤0.40	2			
		0.40＜Φ≤0.60	1			
		Φ＞0.60	0			

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Item No	Items to be inspected	Inspection Standard					Classification of defects
4.2.2	Line defect Black line, White line, Foreign material on polarizer	Size(mm)		Acceptable Qty			Minor
		L(Length)	W(Width)	Zone			
				A	B	C	
		Ignore	W≤0.05	Ignore		Ignore	
		L ≤5.0	0.05＜W≤0.08	2			
			W＞0.08	0			
	Foreign material on TP film	The line can be seen after mobile phone in the operating condition:					Minor
		Size(mm)		Acceptable Qty			
		L(Length)	W(Width)	Zone			
				A	B	C	
		Ignore	W≤0.05	Ignore		Ignore	
		L ≤5.0	0.05＜W≤0.08	3			
			W＞0.08	0			
4.2.3	Dim line defect Polarizer scratch TP film scratch	If the scratch can be seen after mobile phone cover assembling or in the operating condition, judge by the line defect of 4.2.2. If the scratch can be seen only in non-operating condition or some special angle, judge by the following.					Minor
		Size(mm)		Acceptable Qty			
		L(Length)	W(Width)	Zone			
				A	B	C	
		Ignore	W≤0.03	Ignore		Ignore	
		5.0＜ L≤10.0	0.03＜W≤0.05	2			
		L≤5.0	0.05＜W≤0.08	1			
			W＞0.08	0			
4.2.4	Polarize Air bubble	Air bubbles between glass & polarizer					Minor
		Size(mm) \ Zone		Acceptable Qty			
				A	B	C	
		Φ≤0.20		Ignore		Ignore	
		0.20＜Φ≤0.3		2			
		Φ＞0.30		0			

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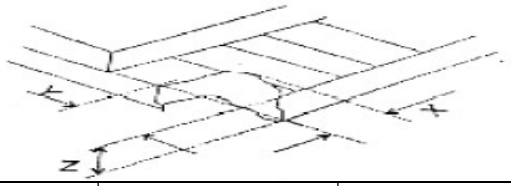
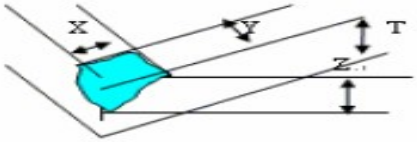
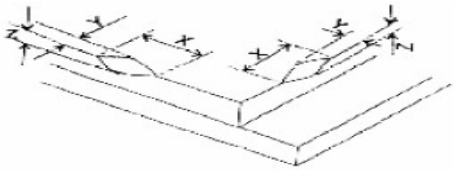
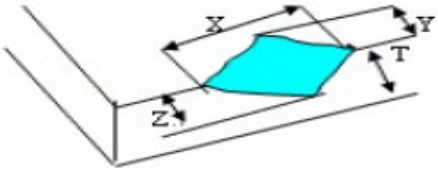
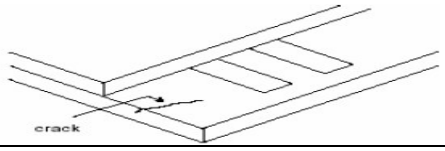
Item No	Items to be inspected	Inspection Standard			Classification of defects
4.35	Glass defect	(i) Chips on corner A:LCD Glass defect  			Minor
		X(mm)	Y(mm)	Z(mm)	
		≤3.0	≤3.0	Disregard	
		Notes: S=contact pad length Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal. B:TP Glass defect  			
		X(mm)	Y(mm)	Z(mm)	
		≤3.0	≤3.0	Disregard	
		(ii)Usual surface cracks A:LCD Glass defect  			
		X(mm)	Y(mm)	Z(mm)	
		≤3.0	<Inner border line of the seal	Disregard	
		B:TP Glass defect  			
		X(mm)	Y(mm)	Z(mm)	
		≤6.0	＜2.0	Disregard	
		(iii) Crack Cracks tend to break are not allowed.  			

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12 PRECAUTIONS FOR USE OF LCD MODULES

1. Handling Precautions

(1) The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.

(2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.

(3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).

(4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.

(5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents

- Isopropyl alcohol
- Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

(6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.

- Water
- Ketone
- Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

(7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

(8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

(9) Do not attempt to disassemble or process the LCD module.

(10) NC terminal should be open. Do not connect anything.

(11) If the logic circuit power is off, do not apply the input signals.

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(12) Electro-Static Discharge Control，Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
- Tools required for assembling, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated

(13)Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- Do not damage or modify the pattern writing on the printed circuit board.
- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist LCM.

2. Storage precautions

2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ～ 40℃ Relatively humidity: ≤80%

2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

2.4 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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